

Transmitted herewith for filing is the Patent Application of: Jayentor: KUAN-LUN CHANG, RUEY-HSIN LIU, TSYR-SHYANG LIOU, CHIH-MIN CHIANG, JUN-LIN TSAI ਤੱ^{or:} A METHOD OF FORMING A SHALLOW TRENCH-DEEP TRENCH ISOLATION REGION FOR A BICMOS/CMOS **TECHNOLOGY** Enclosed are: 6 sheets of drawing(s) - formal. An assignment of the invention to An associate power of attorney Applicant claims small entity status Request & Certification under 35 USC 122(b)(2)(b)(i) The filing fee has been calculated as shown below: (Col. 1) (Col. 2) OTHER THAN A SMALL ENTITY FOR: NO. FILED NO. EXTRA **RATE FEE** BASIC FEE \$ 770. TOTAL CLAIMS 35 - 20= 15 x 18 =\$ 270. INDEP CLAIMS 3 -3= x 86 =\$ 0. **SUB TOTAL** \$ 1,040. ASSIGNMENT \$0. TOTAL \$ 1.040. Please charge my Deposit Account No. 19-0033 in the amount of \$ 1,040. A duplicate copy of this sheet is enclosed.

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